

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Leonard J. Olmer, et al.

Serial No.: 10/675,575

Filed: September 30, 2003

Title: IN-SITU REMOVAL OF SURFACE IMPURITIES PRIOR TO ARSENIC-DOPED POLYSILICON DEPOSITION IN THE FABRICATION OF A HETEROJUNCTION BIPOLAR TRANSISTOR

Grp./A.U.: 1746

Examiner: Jason Mark Heckert

Confirmation No. 9967

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed  
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(Signature of the person signing the certificate)

Sir:

**AMENDMENT UNDER 37 C.F.R. § 1.111**

In response to the Examiner's Action mailed January 14, 2008, please amend the above-identified application as follows: